John Wesley P

Chennai - 600119

SUMMARY

Engineering professional with progressive experience in hardware designing and development of medical devices that comply with international standards with continuous improvement of technology. Experience in all phases of product development from conceptual stages to final product release. Proficient in designing circuits, directing PCB layout, hardware debug, design validation & testing, and documentation. Experience and expertise includes:

- Expertise in EDA tool for schematics design (OrCAD Capture, Altium)
- PCB layout verification (Allegro Physical viewer, PADS and Altium)
- Design validation
- Experienced in EMI/EMC Tests
- Circuit simulation (PSPICE)
- Microcontroller ARM

- Standard communication interfaces such as I2C, SPI, and UART.
- Skilled in development of hardware products from requirement analysis to system study, analog/digital circuit design.

WORK EXPERIENCE

American Megatrends India Pvt. Ltd., Chennai,   **2014-Till Date**
Leader in BIOS, Mobility, Data Storage and Healthcare IT Solutions

Senior Hardware Design Engineer

- Engineering and Technology—Knowledge of applying principles, techniques, procedures, and equipment to the design and production of various devices like general electronics design environment in which processor controlled devices have been developed.
- Design—Knowledge of design techniques, tools, and principles involved in production of precision technical plans, blueprints, drawings, and models.
- Experience in a range of technologies including digital, analogue and power electronics.
- Familiarity with CAD packages for schematic capture and PCB design.
- Experience in Environmental testing and EMC.
- Experienced in writing design documents and test procedures.
- Jointly worked with Firmware development team to test designed hardware and report feedback/bug tracking to firmware team and learning firmware debugging for future role of embedded developer on Hardware platform.
- Researching & developing ideas for new products and design. Experience in Prototype design, Schematic Design, Component Selection, Board Bring-up, Debugging & Testing of boards and BOM generation.

Nokia Networks ,Chennai   **2010-2014**
Telecommunications equipment company

Assistant Product Engineer

- Troubleshooting RF baseband modules, Analog and Digital circuits & Testing.
- Quick Learning of circuits, troubleshooting & testing methods to resolve issues that may have an impact on customer quality and drive for closure.
- Perform the RCA (root cause analysis) of the Boards which are failed in performance Test.
- Assist others within engineering in support of debugging design and production/mfg issues.
- Involving in new Technology Implementation Customize to the Product requirements. Read blueprints to determine requirements to develop a product.
- Configuration for new testers. Handling all kind of RF and Electronics Instruments.RF loss Calibration and validation.RF Tester in home Calibration.

EDUCATION

Bachelor of Technology in Electronics and telecommunication Engineering
Karnataka state Open University, Mysore

Diploma in Electronics
NTTF, Bangalore

SOFTWARE

- **EDA Packages**: Or-Cad Schematic Capture, Allegro PCB layout, PSPICE, and Hyper Lynx.
- **Compiler/Editors**: KEIL, Code Composer Studio, and Rowley Cross Works.
- **Other Tools**: Real Term, PRO-E.
- Basics of C programming.

HARDWARE

Familiarization of using Electronic test Equipment's like Multi-meter, Power Supply, LCR meter, Digital Signal oscilloscope (DSO) , Logic Analyzer, Function Generator 0-10MHZ,Signal Generators, Spectrum Analyzers.

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